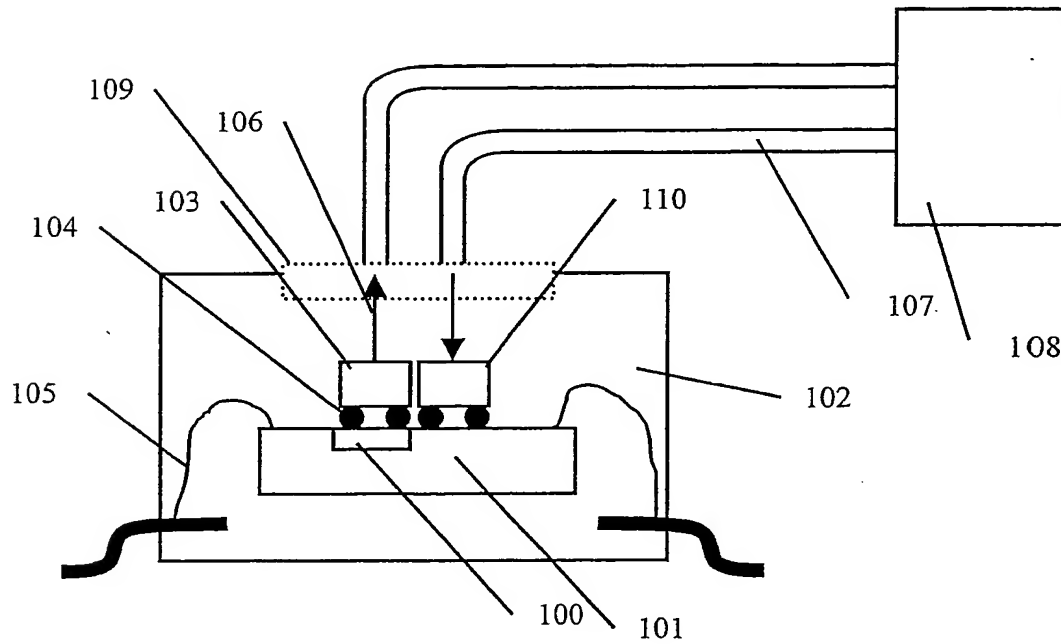


Blakely, Sokoloff, Taylor & Zafman LLP (310) 207-3800
 Title: Integrated Circuit with Debug Support Interface
 1st Named Inventor: Klaus Dieter McDonald-Maier
 Express Mail No.: EV665824127US Docket No.: 5926P048
 Sheet: 1/8

1/8

Figure 1



2/8

Figure 2

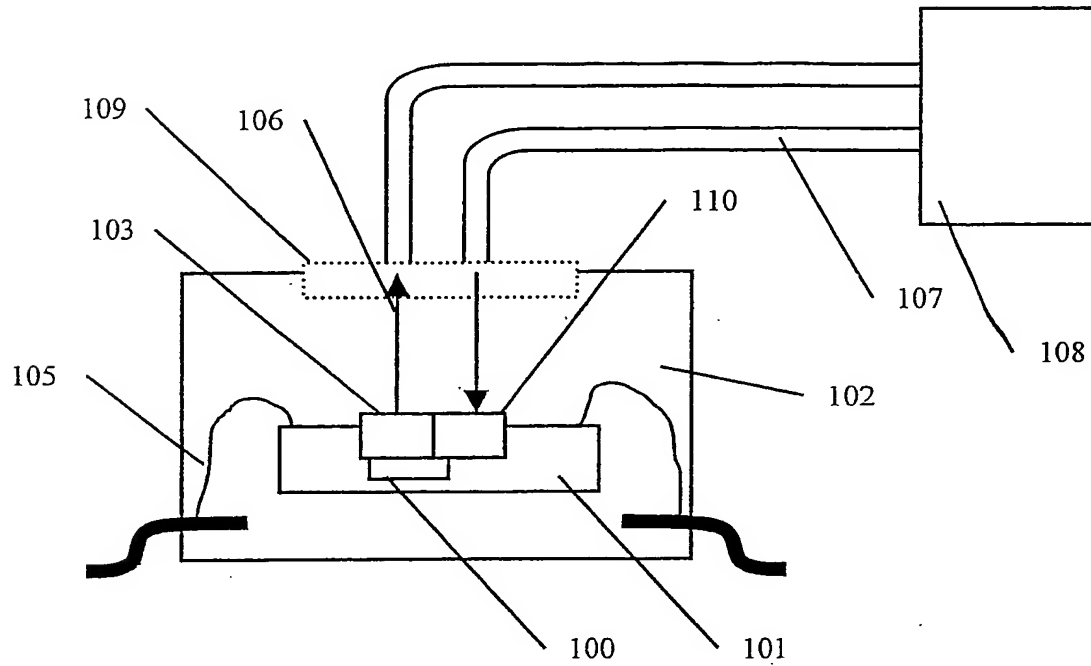
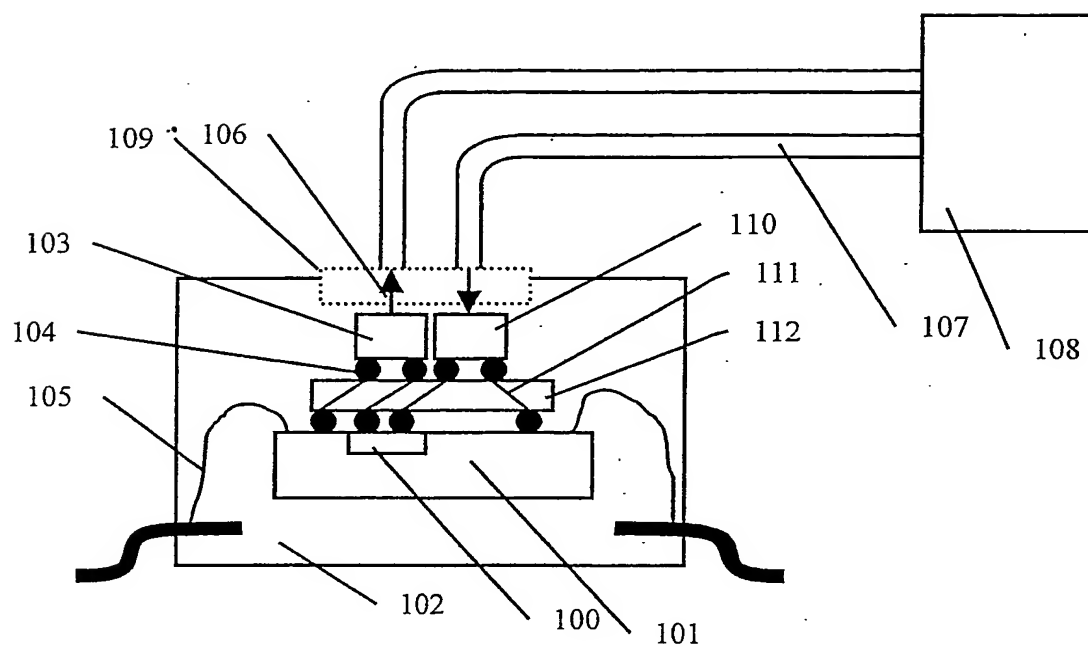


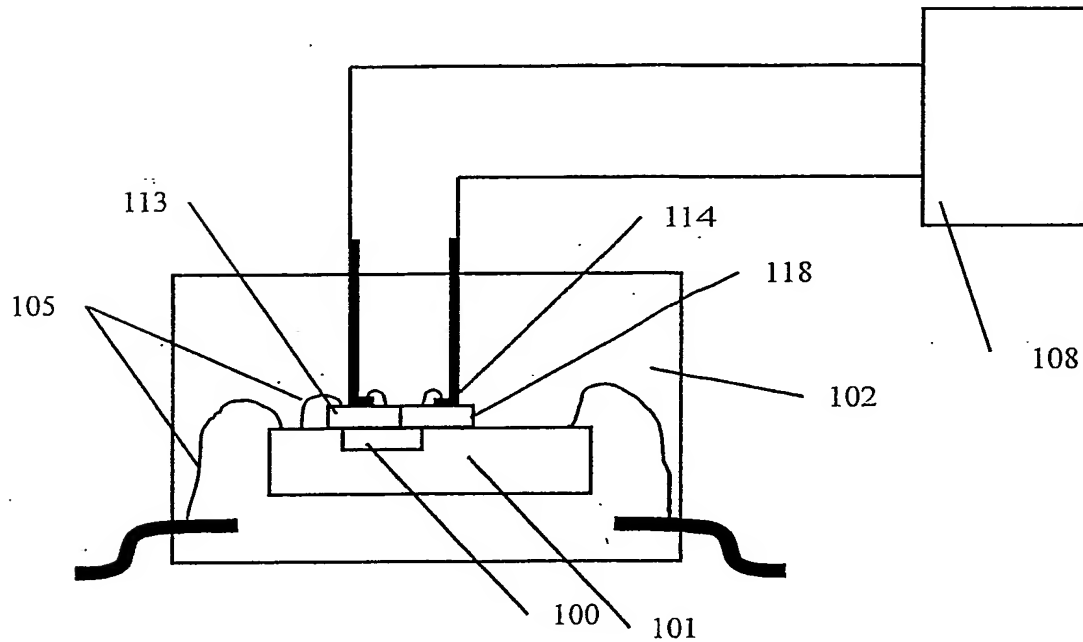
Figure 3



Blakely, Sokoloff, Taylor & Zafman LLP (310) 207-3800
Title: Integrated Circuit with Debug Support Interface
1st Named Inventor: Klaus Dieter McDonald-Maier
Express Mail No.: EV665824127US Docket No.: 5926P048
Sheet: 4/8

4/8

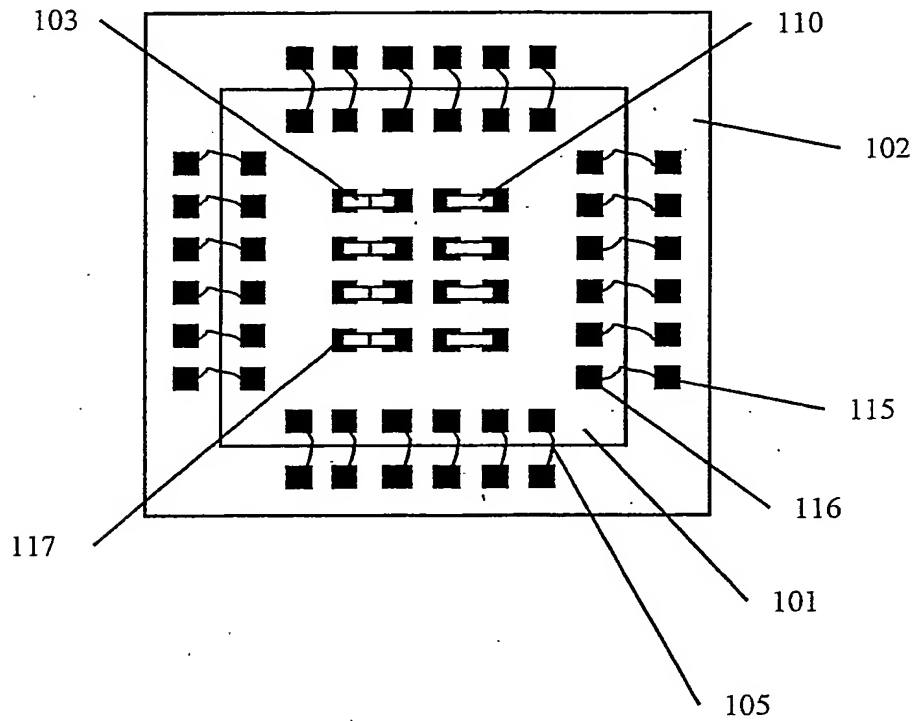
Figure 4



Blakely, Sokoloff, Taylor & Zafman LLP (310) 207-3800
Title: Integrated Circuit with Debug Support Interface
1st Named Inventor: Klaus Dieter McDonald-Maier
Express Mail No.: EV665824127US Docket No.: 5926P048
Sheet: 5/8

5/8

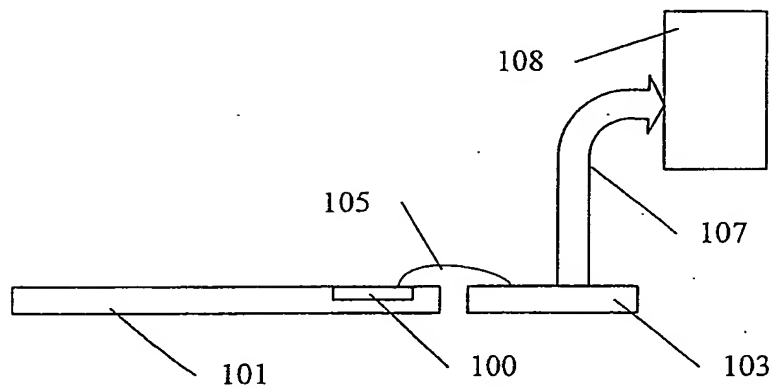
Figure 5



Blakely, Sokoloff, Taylor & Zafman LLP (310) 207-3800
Title: Integrated Circuit with Debug Support Interface
1st Named Inventor: Klaus Dieter McDonald-Maier
Express Mail No.: EV665824127US Docket No.: 5926P048
Sheet: 6/8

6/8

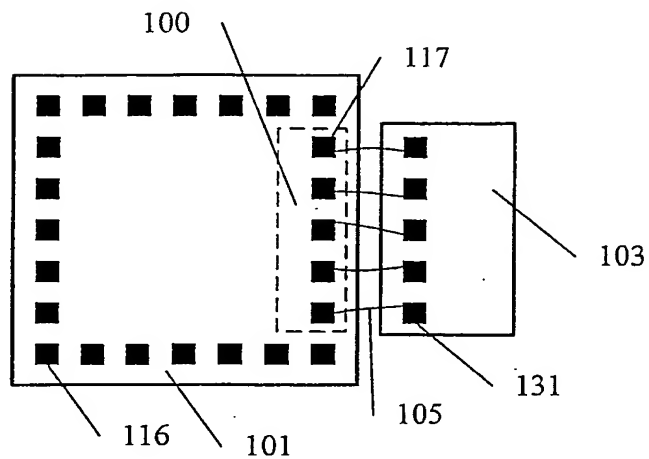
Figure 6



Blakely, Sokoloff, Taylor & Zafman LLP (310) 207-3800
Title: Integrated Circuit with Debug Support Interface
1st Named Inventor: Klaus Dieter McDonald-Maier
Express Mail No.: EV665824127US Docket No.: 5926P048
Sheet: 7/8

7/8

Figure 7



Blakely, Sokoloff, Taylor & Zafman LLP (310) 207-3800
Title: Integrated Circuit with Debug Support Interface
1st Named Inventor: Klaus Dieter McDonald-Maier
Express Mail No.: EV665824127US Docket No.: 5926P048
Sheet: 8/8

8/8

Figure 8

